



SK-AM62A-LP SKEVM

REV A

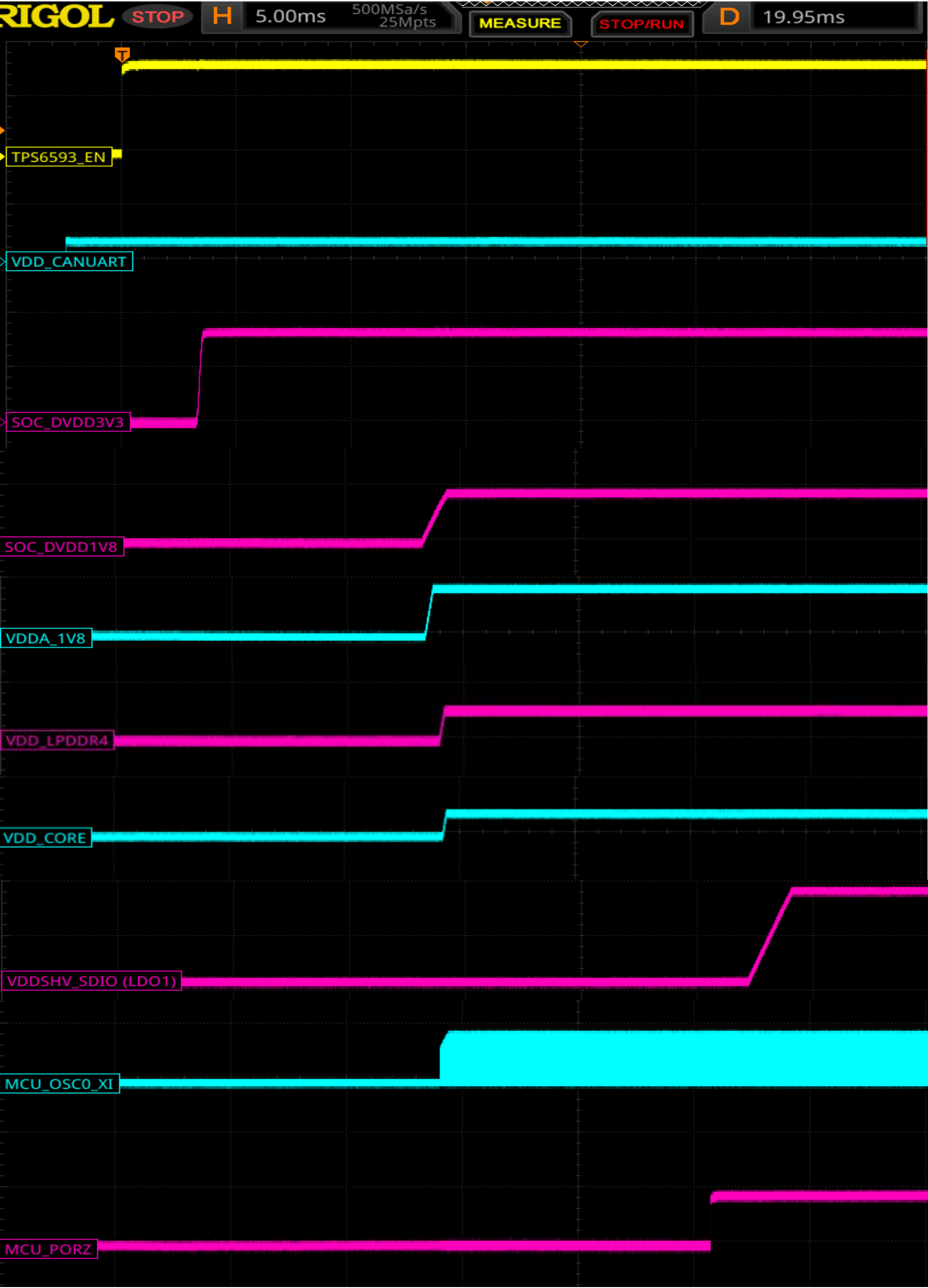
PROC135A

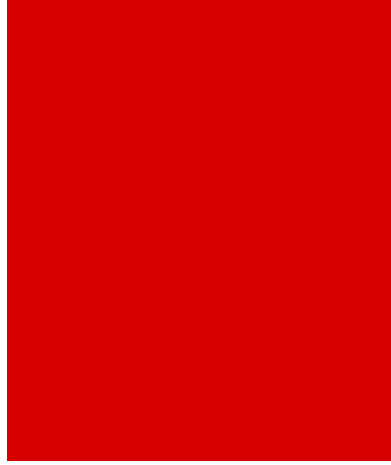
Power up Sequence

Power Up sequence timing measurements



Power Supply	Voltage (V)	Elapsed Time (mS)
PMIC ENABLE	3.3	0
SoC_DVDD3V3	3.3	3.25
VDD_CANUART	0.75	-2.45
VDDA_1V8	1.8	13.2
SoC_DVDD1V8	1.8	13.2
VDD_LPDDR4	1.1	13.9
VDD_CORE	0.75	14.3
VDDSHV_SDIO	3.3	27.3
MCU_OSC0_XI	1.8	14
MCU_PORz (Reset)	1.8	25.8





SK-AM62A-LP SKEVM PROC135A REV A Power Down Sequence

Power Down sequence timing measurements



Power Supply	Voltage (V)	Elapsed Time (mS)
PMIC ENABLE	3.3	0
SoC_DVDD3V3	3.3	23.2
VDDA_1V8	1.8	10.7
SoC_DVDD1V8	1.8	10.7
VDD_LPDDR4	1.1	0.6
VDD_CORE	0.75	11.7
VDDR_CORE	0.75	11.7
VDDSHV_SDIO	3.3	12.2
MCU_OSC0_XI	1.8	10.6
MCU_PORz (Reset)	1.8	0.1



Combined Power sequence diagram

